





PCT

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

(PCT Article 36 and Rule 70)

Applicant's or agent's file reference 03SGL0136WOP	FOR FURTHER ACT		eation of Transmittal of International Examination Report (Form PCT/IPEA/416)			
International application No. PCT/EP2003/003882	International filing date 15 April 2003 (Priority date (day/month/year) 15 April 2002 (15.04.2002)			
International Patent Classification (IPC) or no C23C 14/10			13 April 2002 (13.04.2002)			
Applicant	SCHOTT	ΓAG				
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Contain degree of a	nations supporting such st	regard to novelty, in tatement	nventive step or industrial applicability;			
V1	VI Certain documents cited Certain defects in the international application					
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Date of submission of the demand 29 August 2003 (29.0)	8.2003)	Date of completion 26	of this report August 2004 (26.08.2004)			
Name and mailing address of the IPEA/EP	,	Authorized officer				
Facsimile No.		Telephone No.				



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Internal application No.

PCT/EP2003/003882

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	pages		, filed with the demand					
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I the i	nternation	o the language, all the elements marked above were available or furnished to thin nal application was filed, unless otherwise indicated under this item. Its were available or furnished to this Authority in the following language	is Authority in the language in which which is:					
	the lan	guage of a translation furnished for the purposes of international search (under Ru	ıle 23.1(b)).					
	the lan	guage of publication of the international application (under Rule 48.3(b)).						
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3. Wit	th regard liminary o	to any nucleotide and/or amino acid sequence disclosed in the internate examination was carried out on the basis of the sequence listing:	tional application, the international					
	contai	ned in the international application in written form.	,					
	filed t	ogether with the international application in computer readable form.	ļ					
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	The statement that the subsequently furnished written sequence listing does not go beyond the disclosure in the international application as filed has been furnished.							
	_	tatement that the information recorded in computer readable form is identical furnished.	to the written sequence listing has					
4.	The a	mendments have resulted in the cancellation of:						
1		the description, pages						
1		the claims, Nos.						
	Ш	the drawings, sheets/fig						
5. [This rebeyon	eport has been established as if (some of) the amendments had not been made, s d the disclosure as filed, as indicated in the Supplemental Box (Rule 70.2(c)).**	ince they have been considered to go					
in i	this repo 1 70.17).	t sheets which have been furnished to the receiving Office in response to an invit rt as "originally filed" and are not annexed to this report since they do n	ot contain amendments (Rule 70.16					
** Any	y replace	ment sheet containing such amendments must be referred to under item 1 and ann	exed to this report.					

V.	Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability;
	citations and explanations supporting such statement

Statement			
Novelty (N)	Claims	1-32	YES
	Claims		NO
Inventive step (IS)	Claims	1-32	YES
	Claims		NO NO
Industrial applicability (IA)	Claims	1-32	YES
	Claims		NO

2. Citations and explanations

This report makes reference to the following documents:

- D1: WOO-BEOM CHOI ET AL: "Anodic bonding technique under low temperature and low voltage using evaporated glass", Journal of Vacuum Science & Technology B (Microelectronics and Nanometer Structures), Vol. 15, No. 2, pages 477-481, March-April 1997
- D2: CH 387 175 A (WESTERN ELECTRIC CO) 31 January 1965 (1965-01-31)
- D3: US-A-4 374 391 (CAMLIBEL IRFAN ET AL) 15 February 1983 (1983-02-15)

Document D1 is considered to constitute the prior art closest to the subject matter of claim 1 and discloses (the references in parentheses are to that document): a method for forming the housing of electronic components (abstract; page 481, last sentence) in which an evaporated glass source is used to apply a glass layer on one side of the substrate by vaporisation (page 477, Chapter II). The subject matter of claim 1 therefore differs from the known method in that the opposite side of the substrate is then processed so as to generate line contacts. Similarly, the method as per claim 1 differs from the methods in documents D2 (see claims I; II, 1.1; page 7, line 67 to

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page 8, line 46) and D3 (see column 4, lines 44-51). The subject matter of claim 1 is therefore novel (PCT Article 33(2)).

The present invention can therefore be considered to address the problem of producing connection structures to electronic components already protected by a layer of evaporated glass. The solution to this problem, as proposed in claim 1 of the present application, involves an inventive step (PCT Article 33(3)) because this method cannot be derived from the prior art.

Claims 2-22 are dependent on claim 1 and therefore likewise meet the PCT novelty and inventive step requirements.

Although documents D1, D2 and D3 describe electronic components entirely or partially coated with an evaporated glass layer, the subject matter of claim 23 is not known from or suggested by these citations. According to claim 23, the connection structures on the component are located, owing to its production process, on the side of the substrate away from the glass layer. Claim 23 therefore meets the PCT novelty and inventive step requirements.

Claims 24-32 are dependent on claim 23 and therefore likewise meet the PCT novelty and inventive step requirements.

Since the invention is used in the electronic industry, it is industrially applicable.